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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)  Sheet 1 of 1		Application Number	09/160,657
		Filing Date	September 25, 1998
		First Named Inventor	Joseph W. Lyding
		Art Unit	2822
		Examiner Name	VOCKRODT, Jeff B.
		Attorney Docket Number	UIL-10013C
<b>OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS</b>			
Examiner Initials*	Cite No.1	Include Name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	
JW		A. UCHIYAMA, H. FUKUDA, T. HAYASHI, T. IWABUCHI, S. OHNO "High Performance Dual-gate Sub-halfmicron CMOSFETs with 6nm-thick Nitrided SiO <sub>2</sub> Films in an N <sub>2</sub> O Ambient", <i>IEDM Tech. Dig. 1990, pp 16.6.1 – 16.6.4</i>	
Examiner Signature	[Signature]		Date Considered
			5-2-03

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